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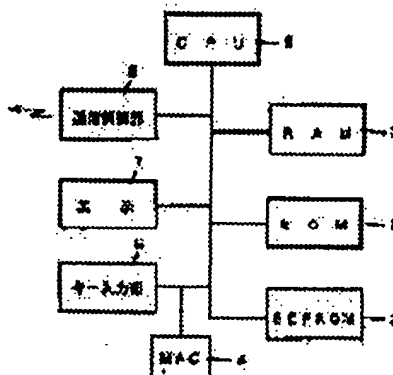
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(54) DATA PROCESSOR

(57)Abstract:

PURPOSE: To confirm whether or not data are normally written by detecting a defect in writing data on the basis of whether identifiers showing the start and end of writing corresponding to the reading of data from a flash memory meet specific requirements each time the data are read out of the flash memory.

CONSTITUTION: When data are written in an EEPROM 2, block by block, in a ROM→RAM→EEPROM setting process and an EEPROM correcting process, the data are written in the EEPROM 2 while sandwiched between a start coded and an end code. When the data are read out of the EEPROM 2 in a subsequent EEPROM→RAM setting process, it is checked whether or not the start code and end code match each other, block by block, to detect a defect in data writing to the EEPROM 2. Consequently, it can be confirmed whether or not abnormality such as a power failure occurs during the writing to the EEPROM 2 each time the data are read out of the EEPROM 2.

LEGAL STATUS

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